B.E. (Information Technology) Semester Seventh (C.B.S.) **Elective - I : Compiler Design**

P. Pages: 3			KNT/KW/16/75	504
Time : Three Hours		ee Hours	Max. Marks	
	Notes: 1.		All questions carry marks as indicated.	
		2.	Solve Question 1 OR Questions No. 2.	
		3.	Solve Question 3 OR Questions No. 4.	
		4.	Solve Question 5 OR Questions No. 6.	
		5.	Solve Question 7 OR Questions No. 8.	
		6.	Solve Question 9 OR Questions No. 10.	
		7.	Solve Question 11 OR Questions No. 12.	
		8.	Due credit will be given to neatness and adequate dimensions.	
(0		9.	Assume suitable data whenever necessary.	
70		10.	Illustrate your answers whenever necessary with the help of neat sketches.	
1.	a)	With su	itable schematic, explain structure of realistic compiler.	7
	b)		the advantages of analysis - synthesis model of compilation. Also give example to e your answer.	6
			OR	
2.	a)		ct transition diagram and regular expression for the following -	6
		i) ide	entifier ii) Unsigned number (integer and real number).	
				_
	b)		e the tools that can aid in the process of compilation? Explain each tool, briefly. ex I/p specifications used to represent Lexical constructs of programming language.	7
3.	o)	Whatan	e the issues of CFG for the programming languages that need to be considered	
	a)		esigning top down parser?	6
		wille de	signing top down parser:	N.
	b)	What do	you mean by recursive descent parser? Design recursive descent parser for the	5
	U)		ng grammer - $S \rightarrow aCa$, $C \rightarrow c/b$.) -
		TOHOWIL	ig grammer 5 7 deat, C 70/0.	
	c)	Comput	the the first and follow set of A in the following grammar - $A \rightarrow (A)A/\in$.	2
	c)	Comput		3
4	-)	XX 7	OR	
4.	a)	write ar	algorithm for construction of parsing table for SLR parser.	6
	b)	Constru	ct LALR parsing table for the following grammar with S as a start symbol.	8
	b)	$S \rightarrow AA$		o
		$A \rightarrow aA$	r/p	
_			ala(0)	_
5.	a)		inslation scheme for the following programming language constructs -	6
		1) For	r loop ii) IF-Then-Else iii) While-do	
	20			\mathcal{I}
16	b)	- / /	OTS for the following statement and translate the following statement into	8
15	0)		diate code.	
			=B[I,J]+C[I+J+K] where	
			O array of size 10 * 10,	
			array of size 10*10,	
		C is 1-D	O array of size 30 and BPW = 4	

6. a) Give translation scheme and generate three address code and parse tree of the following program segment -

While (A < C and B < D) do{ if A == 1 then C = C + 1else { while A <= D do A = A + 3}

- b) Explain the data structures used for representation of three address code and compare them
- them.
- b) Discuss various data structures used for implementation of symbol table and compare

What are the various attributes that should be stored in symbol table.

OR

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8. a) What do you mean by phrase level error recovery ? Explain phrase level error recovery scheme for LL parser with grammar.

 $E \rightarrow TE'$

them.

a)

 $E' \rightarrow +TE'/ \in$

 $T \rightarrow FT'$

 $T' \rightarrow *FT' / \in$

 $F \rightarrow id$

Also show the behavior of above parser for the input w = id + *id\$.

- b) Explain how scope information is represented in symbol table for block structured programming languages.
- **9.** a) Give an algorithm to find out the iterative solution of data flow equations.

b) Consider the flow graph -

 $i = m_1$ j = n $a = u_1$ B_2 $a = u_2$ B_3 $i = u_3$ B_4

Find IN and OUT for the above example.

OR

Explain clearly how DAG can be used for code optimization for above code.

- b) Explain in detail the principle sources of optimization. Give proper examples.
- 11. a) What is the role of register descriptor and address descriptor in simple code generation algorithm? Give steps involved in simple code generation algorithm.
 - b) Consider the expression -

$$a = (p+q)-((r+s)-t)$$

Answer the following-

- i) Give its three address code.
- ii) Generate code using simple code generation algorithm.

OR

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12. a) Consider the three address code

$$t_1 = a + b$$

$$t_2 = c + d$$

$$t_3 = e - t_2$$

$$\mathbf{t}_{4} = \mathbf{t}_{1} - \mathbf{t}_{3}$$

- i) Construct DAG representation for the above code.
- ii) Apply labelling algorithm to determine number of registers required to generate a code.
- b) What do you mean by peephole optimization? Explain it in detail.

