B.E. (Computer Technology) Sixth Semester (C.B.S.)

Embedded System Design

P. Pages: 2

TKN/KS/16/7491

P. Pages Time : Tl				Max. Marks : 80	
	Note	es: 1. 2. 3. 4. 5. 6. 7. 8. 9. 10.	All questions carry marks as indicated. Solve Question 1 OR Questions No. 2. Solve Question 3 OR Questions No. 4. Solve Question 5 OR Questions No. 6. Solve Question 7 OR Questions No. 8. Solve Question 9 OR Questions No. 10. Solve Question 11 OR Questions No. 12. Due credit will be given to neatness and adequate dimensions. Assume suitable data whenever necessary. Illustrate your answer whenever necessary with the help of neat sketches.		
1.	a)	List an	d explain Embedded system project Management's four components.	5	
	b)	Why R	TOS is required for an Embedded system? Can ES work without RTOS?	5	
	c)	Define	Emulator with proper example.	3	
2.	a)	Explain system	OR n design cycle with proper diagram in the development phase for an Embedded .	7	
	b)	i) In	a short note on followingcircuit emulator. se of software tool for development of an ES.	6	
3.	a)	Explain	n concept of semaphore. Draw and explain in detail execution flow with semaphore.	8	
	b)	you fix St V { O iR } V	llowing routines are called by Task A,B and C, but they don't work. How would the problem? satic int iRecordCount; oid increment_records (int iCount) SSemGet (SEMAPHORE_PLUS); tecordCount + = iCount; oid decrement_records (int iCount) tecordCount - = iCount; SSemGive (SEMAPHORE _ MINUS)	6	
4.	a)	Write a	OR a short note on:		
~.	/		riority inversion problem.	4	
			lutex.	3	
		,	irtual sockets.	3	

	b)	Draw and explain task states in RTOS.	4
5.	a)	Give the detail Case Study of Digital Camera.	
	b)	Explain the following question with proper diagram regarding interrupt in 8051. i) How interrupt routine should work? ii) What would really happen? iii) How interrupt routine do work?	6
6.	a)	OR Give the detail Case Study of GPS.	7
U.	b)	Explain Real time Operating System services in detail.	6
	0)	Emplain feeli time operating system services in detail.	v
7.	a)	Show the content of the PSW register after execution of the following instructions. MOV A, # 0BFH ADD A, # 1BH	6
	b)	For an 89C51 with a crystal frequency of 22 MHz. find a time delay for the following loop inside loop. DELAY: MOV R2, # 19 HERE: MOV R3, # 255 AGAIN: DJNZ R3, AGAIN DJNZ R2, HERE RET	8
0	,	OR	0
8.	a)	Write a short note on: any two. 1) CALL Instructions. 2) 8051 Register Banks. 3) 8051 Counter.	8
	b)	Explain various addressing modes of 8051.	6
9.	a)	Explain serial port control register with proper diagram.	6
	b)	Write a short note on basics of serial communication, OR	7
10.	a)	Write a program to generate two square wave - one of 5KHz frequency at pin P 1.3 & another of freq. 25 KHz at pin 2.3. Assume XTAL = 22 MHz.	8
	b)	Explain TCON register and its role in handling interrupts.	5
11.	a)	Explain in detail various kinds of semi conductor memories & their characteristics.	6
	b)	Draw and explain interfacing an LCD to the 8051. OR	
12.	a)	Draw and explain interfacing a DAC to the 8051.	7
	b)	Draw and explain 8051 interfacing to the keyboard.	6
